



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:  
David L. Nilsen et al.

Serial No. 10/616,735

Filed: 07/10/2003

For: Microcomponent Interconnection  
Utilizing Post-Assembly Activation

§ Attorney Docket No. 34003.77  
§  
§ Customer No. 27683  
§  
§ Group Art Unit: Unknown  
§  
§ Examiner: Unknown  
§

**INFORMATION DISCLOSURE STATEMENT**

Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In compliance with the duty of disclosure under 37 CFR §1.56, and in accordance with the practice under 37 CFR §1.97 and §1.98, the Examiner's attention is directed to the documents listed on the enclosed modified Form PTO-1449. No inference should be made that the cited references are in fact material, are in fact prior art, or that no better art exists. The cited documents are listed in numerical order and are not in any order based on their pertinence.

The Commissioner is hereby authorized to charge any additional fees which may be required or credit any overpayment to Deposit Account 08-1394.

It is respectfully requested that the above information be considered by the Examiner and that a copy of the enclosed Form PTO-1449 be returned indicating that such information has been considered.

Respectfully submitted,

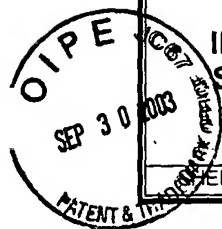
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Date: September 25, 2003  
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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner For Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on September 25, 2003.

  
Gayle Conner



In place of  
PTO-1449  
Form

U. S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE

Complete if Known

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Application Number	10/616,735
Filing Date	July 10, 2003
Applicant(s)	Erik Nilsen et al.
Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	34003.77

SHEET 1 OF 1

**U. S. PATENT DOCUMENTS**

Examiner's Initials	Cite No.	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
	AA	6,131,277	10/17/2000	Cadenhead et al.
	AB	6,193,139	02/27/2001	Kivilahti
	AC	6,429,113	08/06/2002	Lewis et al.

**FOREIGN PATENT DOCUMENTS**

Examiner's Initials	Cite No.	Foreign Patent Document (Country Code - Number - Kind)	Publication Date MM-DD-YYYY	Patentee or Applicant of Cited Document	Translation Y/N

**OTHER PRIOR ART**

Examiner's Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item, date, page(s), volume, issue number(s), publisher, city/country where published
	AD	Harsh et al., "Solder Self-Assembly for MEMS," Proceedings of the 1998 International Instrumentation Symposium (ISA'98), Reno, NV, pp. 256-261, May 3-7, 1998.
	AE	Harsh et al., "Flip-Chip Assembly for Si-Based RF MEMS," Proceedings of the 1999 IEEE International Conference on Microelectromechanical Systems (MEMS '99), Orlando, FL, pp. 273-278, January 17-21, 1999.
	AF	Gracias et al., "Forming Electrical Networks in Three Dimensions by Self-Assembly," <i>Science</i> , Volume 289, pp. 1170-1172, August 18, 2000.

Examiner  
Signature

Date  
Considered

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.